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**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re the application of: )  
BAILEY III et al. ) Group Art Unit: 2811  
Application No.: 10/769,498 ) Examiner: Unassigned  
Filed: January 30, 2004 ) Atty. Docket No.: LAM2P447  
For: STRESS FREE ETCH PROCESSING IN ) Date: July 23, 2004  
COMBINATION WITH A DYNAMIC LIQUID )  
MENISCUS )

**CERTIFICATE OF MAILING**

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450 Alexandria, VA 22313-1450 on July 23, 2004.

Signed: \_\_\_\_\_

Melinda M. Ward

**INFORMATION DISCLOSURE STATEMENT**

Commissioner for Patents  
Alexandria, VA 22313-1450

Dear Sir:

The references listed in the attached PTO Form 1449, copies of which are attached, may be material to examination of the above-identified patent application. Applicants submit these references in compliance with their duty of disclosure pursuant to 37 CFR §§ 1.56 and 1.97. The Examiner is requested to consider these references and to acknowledge such consideration by initialing the appropriate location on the attached PTO Form 1449.

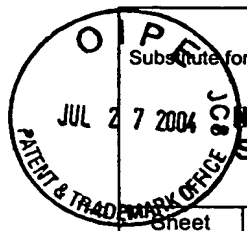
This Information Disclosure Statement (IDS) is not to be construed as a representation that a search has been made, that additional information material to the examination of this application does not exist, or that these references indeed constitute prior art.

This IDS is believed to be filed before the mailing date of a first Office Action. Accordingly, it is believed that no fees are due in connection with the filing of this IDS. However, if it is determined that any fees are due, then the Commissioner is hereby authorized to charge such fees to Deposit Account 50-0805 (Order No. LAM2P447).

Respectfully submitted,  
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Substitute for form 1449/PTO

# **INFORMATION DISCLOSURE STATEMENT BY APPLICANT** (Use as many sheets as necessary)

Sheet

1

of

1

**Complete if Known**

Application Number	10/769,498
Filing Date	January 30, 2004
First Named Inventor	Bailey
Art Unit	2811
Examiner Name	Unassigned
Attorney Docket Number	LAM2P447

**U.S. Patent Documents**

Examiner Initials	Cite No. <sup>1</sup>	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, where Relevant Passages or Relevant Figures Appear
		Number-Kind Code <sup>2</sup> (if known)			
	A	4,838,289	06/1989	Kottman et al.	
	B	5,271,774	12/1993	Leenaars et al.	
	C	5,705,223	01/1998	Bunkofske	
	D	5,945,351	08/1999	Mathuni	
	E	5,997,653	12/1999	Yamasaka	
	F	6,398,975	06/2002	Mertens et al.	
	G	6,491,764	12/2002	Mertens et al.	
	H				

**Foreign Patent Documents**

Examiner Initial	Cite No. <sup>1</sup>	Foreign Patent Document No.	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, where Relevant Passages or Relevant Figures Appear	T <sup>6</sup>
		Country Code <sup>3</sup> -Number- <sup>4</sup> Kind Code <sup>5</sup> (if known)				
	I	JP02280330	11/1990	Mitsubishi Electric Corp.		<input checked="" type="checkbox"/>
	J	JP02309638	12/1990	Fujitsu LTD		<input checked="" type="checkbox"/>
	K	JP11350169	12/1999	Chemtronics Co.		<input checked="" type="checkbox"/>
	L					<input type="checkbox"/>
	M					<input type="checkbox"/>

**Non Patent Literature Documents**

Examiner Initial	Cite No.	Include name of author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published
	N	DR. VICTOR KU, 'Low-k Integration Challenges in the Year 2000', Industry Information - What's Up From SEMI - Industry Report, <a href="http://dom.semi.org/web/swemi.nsf/webdocs/9221127CB15550BB88256A4900738B6F">http://dom.semi.org/web/swemi.nsf/webdocs/9221127CB15550BB88256A4900738B6F</a>
	O	INTEL, 'Intel 1.2 GHX All copper Pentium 3 Microprocessor Structural Analysis', Chipworks Reports, <a href="http://www.chipworks.com/reports/flyers/Intel_P3_013.htm">http://www.chipworks.com/reports/flyers/Intel_P3_013.htm</a>
	P	SHIM NAMWOO, "Introduction To InterMetallic Dielectrics & Cu Integration for beginners in our lab."
	Q	

Examiner Signature		Date Considered	
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<sup>1</sup>Applicant's unique citation designation number (optional). <sup>2</sup>See Kinds Codes of USPTO Patent Documents at [www.uspto.gov](http://www.uspto.gov) or MPEP 901.04. <sup>3</sup>Enter Office that issued the document, by two-letter code (WIPO Standard ST.3). <sup>4</sup>For Japanese patent documents, the indication of the year of the reign of Emperor must precede the serial number of the patent document. <sup>5</sup>Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. <sup>6</sup>Applicant is to place a check mark here if English language translation is attached.